

# Reliability Analysis of Converters

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**Abstract:** Performance of power electronic converters has become a major concern in the present day. Converters should be designed with the objective of gaining a high efficiency, increased reliability and less complexity. This paper presents the reliability analysis of three converters. Reliability of two-stage Interleaved boost converter, two-stage and single-stage PFC converter is measured. It is shown that number of components in a device effects the reliability of a system.

**Keywords:** Reliability. MTBF, Power factor correction.

## I. INTRODUCTION

Reliability has become the first priority in the design of a power system. A design is considered successful only if the system is cost effective, efficient and has a greater life time. Reliability of power system is one of the more focused topics today. A power converter consists of a collection of electronic components. None of the elements are perfect, and can fail as time passes and affect the operation of a system. Reliability is the measure of explaining how often these failures occur. In big industries, the lifetime of a system, the failure rates of each component and the overall cost is computed to explain how reliable a system is. It will not be good choice to implement a system practically, if a system is technically acceptable but has less reliability. Large number of components, internal and external stresses, types of the component, quality of the component affects the overall reliability of a system.

This paper presents the reliability analysis of three converters. Two-stage interleaved boost converter, single-stage flyback PFC converter and two-stage Boost-forward converter are designed and simulated using Orcad software. Failure rates of each component in the system are computed. The overall reliability of a system is calculated and the three converters are compared in terms of reliability. It is shown that, system with less number of components has a greater lifetime when compared to those with more number of components.

## II. CIRCUIT TOPOLOGIES

Two-stage interleaved boost converter [5] is suitable for renewable sources of energy. For proper operation of a two-stage interleaved boost converter, the gating signals given to two MOSFET's should be phase shifted by 180°. The current from the supply is divided between two branches. The input current ripples are minimum for a duty ratio of 0.5 [1]. Circuit of a interleaved boost converter is shown in figure 1. Boost-forward converter [6] has two stages: boost converter on the input side and a forward converter on the output side. A turn's ratio of 0.5 is chosen to minimise the stresses.

A single stage flyback converter [6] works similar to that of a ordinary flyback converter. The only difference is that, in this converter if the voltage on the primary winding exceeds the power supply voltage then the diode D1 and D2 will return the excess energy back to the power supply. A turn's ratio of 0.5 is chosen to minimise the stresses on the switches [2]. This two switch flyback converter has a high efficiency and wide operating range [3]. An easy way to comply with the conference paper formatting requirements is to use this document as a template and simply type your text into it.

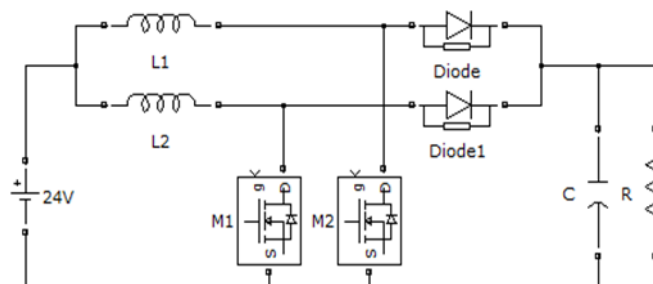


Fig 1: Two-stage Interleaved boost converter

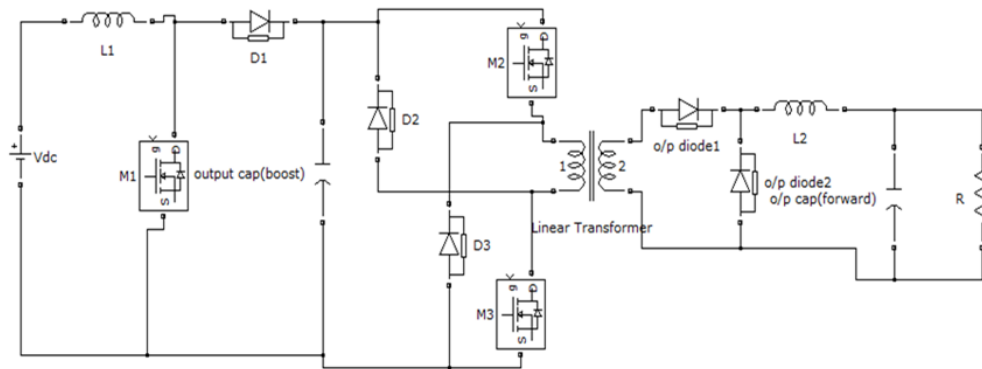


Fig 2: Two-stage Boost forward converter

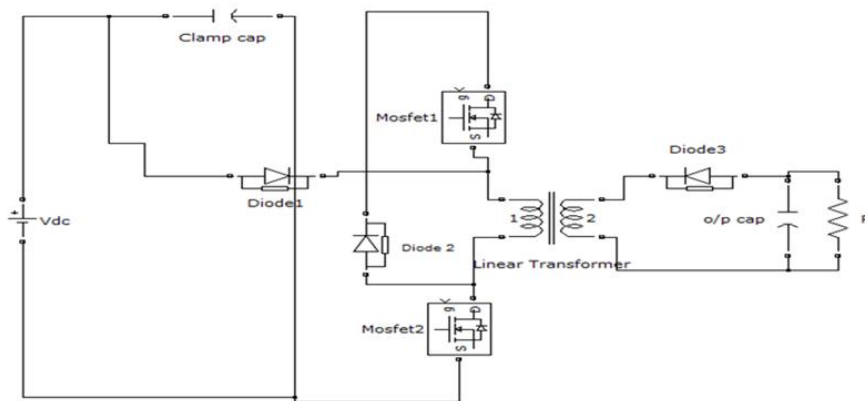


Fig 3: Single stage flyback converter

**III. SIMULATION RESULTS**

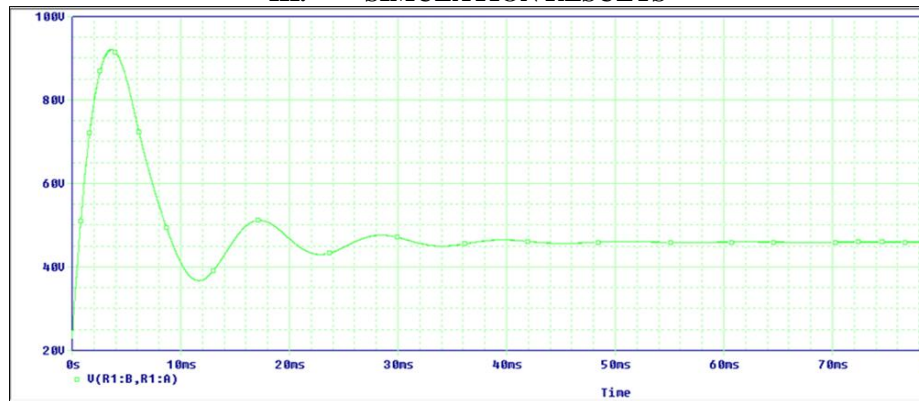


Fig 4: Output voltage of Interleaved boost converter

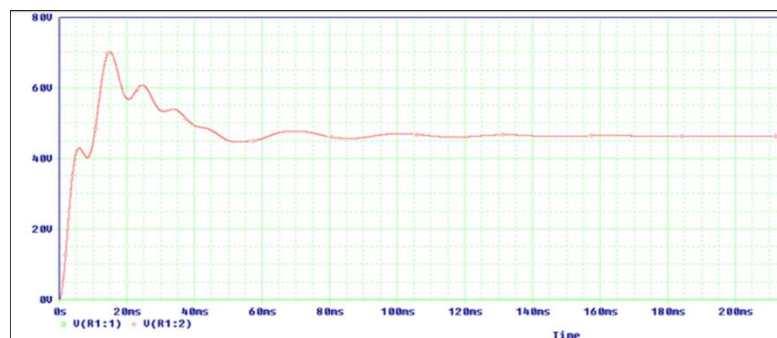


Fig 5: Output voltage of Two-stage boost-forward PFC converter

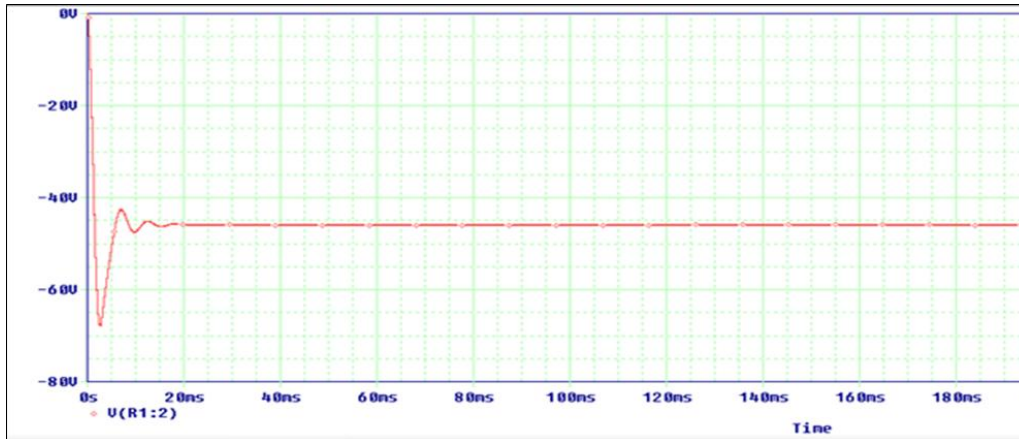


Fig 6: Output voltage of single-stage flyback converter

The circuits were designed for a output voltage of 48V (nominal) and 350W power (nominal). Circuits were designed using Orcad software. Output voltage of 47V was obtained in all the three cases. Using the data obtained from the simulation failure rate was calculated for each component.

#### IV. RELIABILITY CALCULATIONS

The mean time between failures MTBF is expressed as the reciprocal of the failure rate  $\lambda$ .  $\lambda$  is calculated by following the procedure given in MIL-HDBK 217 [4]. The failure rate  $\lambda_b$  for various electronic devices is listed in this handbook. In order to calculate the reliability of any system, this failure rate  $\lambda_b$  must be multiplied by the  $\pi$  factors that define the stresses that a device undergoes. The total failure rate of a system is the sum of the failure rate of all the individual components in the system. The reliability of the overall system is then expressed as the reciprocal of the overall failure rate of the system. Table 1 shows the equations to calculate reliability as expressed in MIL-HDBK 217.

MOSFET undergoes four types of stresses; related to application (switching, linear, power etc.), temperature, quality (low, high) and environment (aerospace, military, ground fixed, laboratory etc.). Capacitor includes stresses related to capacitance value, operational environment, quality of the capacitor, temperature and vottage rating.

TABLE 1: MODELS OF RELIABILITY

Device	Models
MOSFET	$\lambda_p = \lambda_b * \pi_A * \pi_T * \pi_Q * \pi_E$
Diode	$\lambda_p = \lambda_b * \pi_S * \pi_T * \pi_C * \pi_Q * \pi_E$
Inductor	$\lambda_p = \lambda_b * \pi_Q * \pi_E$
Resistor	$\lambda_p = \lambda_b * \pi_R * \pi_Q * \pi_E$
Capacitor	$\lambda_p = \lambda_b * \pi_{CV} * \pi_Q * \pi_E$
Transformer	$\lambda_p = \lambda_b * \pi_C * \pi_Q * \pi_E$

Once the component type, value and the quality is known failure rate can be easily calculated by referring the MIL-HDBK 217. Ambient temperature of 27°C is considered. To calculate the reliability of a device, first the static and dynamic power losses of semiconductors should be considered. Following data is obtained by calculating the failure rate.

TABLE 2: RESULTS FROM RELIABILITY CLCULATIONS

	Interleaved boost	Boost-Forward	Single-stage Flyback
Number of components	8	14	9
Failure rate $\lambda_{system}$ (failure/ $10^6$ h)	7.68702	21.22078	14.83794
MTBF = $\frac{1}{\lambda}$ hours	1,30,089	47,123	67,394

## V. CONCLUSION

In this paper, reliability of two-stage interleaved boost converter, Single-stage flyback converter and two-stage boost forward PFC converter is calculated. The reliability calculations are done by referring the MIL-HDBK 217. From table 2, we can conclude that as the number of components decreases, reliability of a system increases.

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