

Implementation of MIN/MAX functions using Operational Transconductance Amplifier

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Abstract: This paper shows the different steps to realize and simulate the MIN/MAX fuzzy operators using operational transconductance amplifier, operational amplifier and 2 stage CMOS operational amplifier using SPICE. This paper also shows the calculation of different parameters like slew rate, power dissipation, CMRR and gain for all the fuzzy operators. Later comparison of all parameters was discussed. The results with OTA comes out to be the best as it uses less voltage, more slew rate for high speed operations.

Keywords: Operational transconductance amplifier, CMOS operational amplifier, MIN, MAX.

I. INTRODUCTION

An operational amplifier (op-amp) is a directly coupled amplifier which includes different blocks like : dual input balanced output, dual input unbalanced output, level translator and a push pull amplifier. An op-amp can amplify both AC and DC and helps in performing all mathematical operations.[1, 2, 3].

The two stage operational amplifier [4, 5] consists of four different parts as shown in Fig 1 :

1. Differential amp which converts voltage to current
2. Current mirror circuit which converts current to voltage.
3. Transconductance amplifier with gate grounded which converts voltage to current.
4. Class A amplifier with source or sink load which converts current to voltage.

Operational Transconductance Amplifier (OTA) is also called as voltage controlled current source (VCCS) [3, 6, 7, 8]. Fig 2 (a) shows the use of a OTA which is represented by the circuit within the shaded box. R_i is the input resistance, G_m is the gain, and R_o is the output resistance. R_s represents the resistance of the external source V_s and R_L represents the load resistance. The VCCS gain can be expressed as

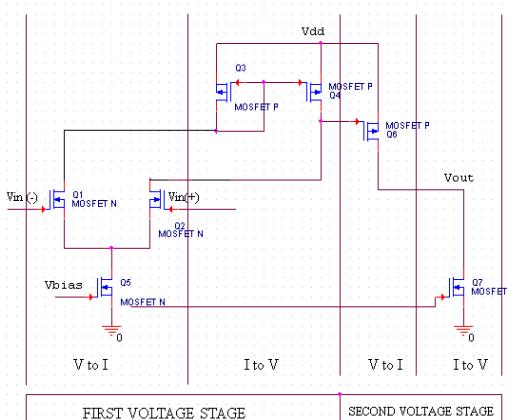


Fig 1 : Two stage CMOS operational amplifier

$$G_M = \frac{G_m R_o R_i}{(R_i + R_s)(R_o + R_L)} \quad \dots(1)$$

For an ideal VCCS, R_i and R_o should be infinite so that G_m approaches G_M . The architecture which will implement the OTA should have high input impedance, high transconductance gain, and high output impedance. Fig 2 (b) illustrates the OTA architecture.

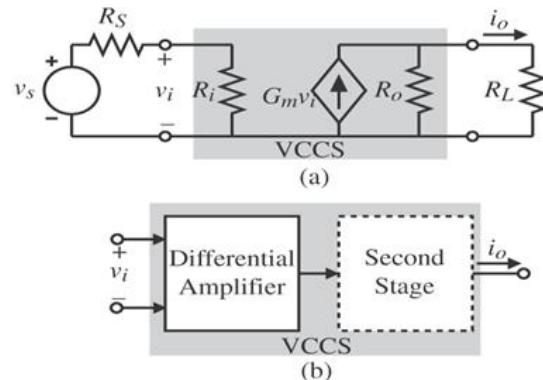


Fig 2 VCCS (a) Circuit (b) Possible architecture

The input stage is differential amplifier with current mirror load. If we need large gain, then second stage was used as inverter otherwise not required. But if we need large gain and high output impedance than cascade load was used as second stage. The block diagram is shown in Fig 2(b).

The fuzzy set is represented as [9, 10]

$$A = \{x, \mu_A(x)\} \quad x \in X \quad \dots(2)$$

where x is an element in X and $\mu_A(x)$ is the membership function of set. The fuzzy set using discrete membership function is expressed as :

$$A = \sum_{x_i \in X} \mu_A(x_i) / x_i \quad \dots(3)$$

The fuzzy set using continuous membership function is expressed as :

$$A = \int \mu_A(x) / x \quad \dots(4)$$

Fuzzy system is divided into five parts as shown in Fig 3.

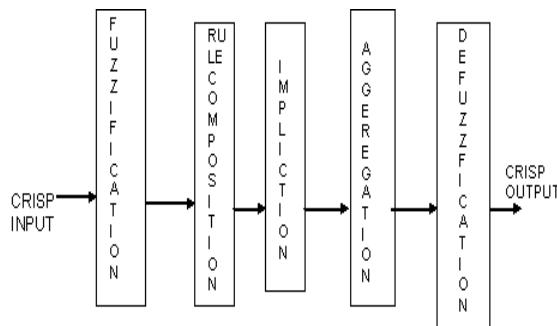


Fig 3 Fuzzy System

This paper discusses the various steps for realizing MIN/MAX fuzzy operator , implementing it using different methods like OTA, 2 stage CMOS op-amp and op-amp using BJT and finally simulating it with PSPICE software. PSPICE is used to calculate voltage and current waveform using different analysis like Transient Analysis, AC Analysis, DC analysis etc [11].

II. MATERIAL AND METHODOLOGY

This section deals with the analysis and design of the MAX & MIN circuits. The electronic implementation of these circuits is essential as these are the building blocks of FLC.

1) MAX circuit : A two input MAX ckt can be realized by using the OR circuit output to the non-inverting terminal of an op-amp.

Let V_1 and V_2 the two voltages to the diodes D_1 & D_2 respectively. If $V_1 > V_2$, then the diode D_1 will be more forward biased and voltage $V_A = V_1 - V_\gamma$ will appear at node A, where V_γ is the biased voltage of diode D_1 .

In order that the voltage input at node B, V_B , to the non inverting terminal is equal to the V_1 , a diode D_3 , similar to diodes D_1 and D_2 is connected in parallel, across node A, so that the forward bias voltage (cut-in voltage, V_γ) developed across D_3 is added at node B to the output from the diode logic OR gate.

As a result, the op-amp effectively behaves as an adder circuit, with a gain of 1, in which the voltages V_A and V_γ are added and the output V_O is obtained, which is the maximum of the two voltages V_1 and V_2 .

For $V_1 > V_2$

Voltage at node A, $V_A = V_1 - V_\gamma$

Voltage developed across diode D_3 at node C, $V_C = V_\gamma$

Using the superposition theorem at node B, The voltage at node B, due to V_A , when $V_C = 0$ is given by

$$V_{BA} = (V_A/R + R)* R = V_A/2 \quad ..(5)$$

The voltage at node B, due to V_C , when $V_A = 0$ is given by

$$V_{BC} = (V_C/R + R)* R = V_C/2 \quad ..(6)$$

Therefore,

$$V_B = V_{BA} + V_{BC} = (V_A + V_C)/2 \quad ..(7)$$

Now, the output of the op-amp is given by

$$V_O = (1 + R_F/R_1)* (V_A + V_C)/2 \quad ..(8)$$

where $(1 + R_F/R_1)$ is the gain of the non-inverting amplifier

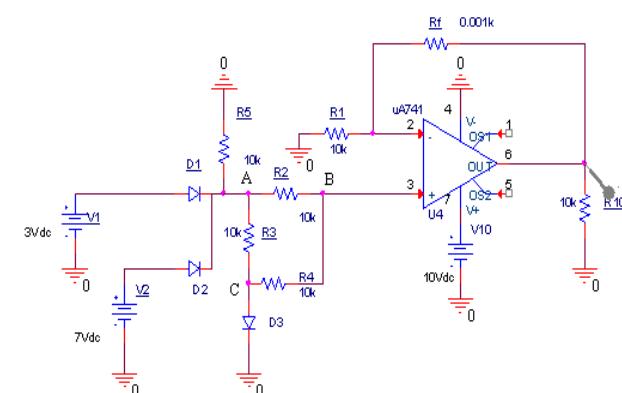


Fig 4 Circuit Diagram for MAX operator using op-amp

For $R_F = R_1$

$$V_O = 2 * (V_A + V_C)/2$$

$$V_O = V_A + V_C \quad ..(9)$$

$$V_O = V_1 \quad ..(10)$$

Thus, the output V_O of the op-amp is equal to the MAX of the two voltages V_1 and V_2 .

Now let us assume that $V_2 = 7$ volts and $V_1 = 3$ volts, then the circuit shown in Fig 4 will give us

$$V_O = \text{Max}(7, 3) = 7 \text{ volts}$$

Its corresponding output illustrates in Fig 5

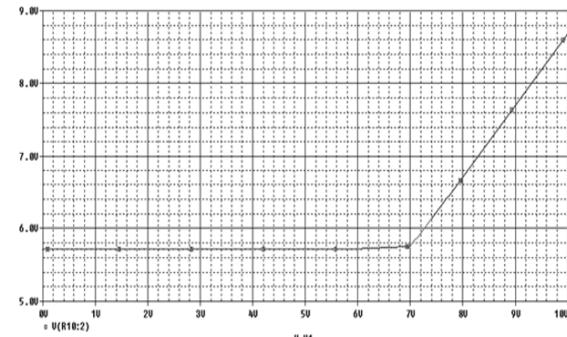


Fig 5 Output corresponding to MAX operator using op-amp

Similarly, we have designed MAX function using 2 stage op-amp (result shown in fig 6) and OTA (result shown in fig 7) and also calculated its electrical parameters shown in fig 8. Fig 8 illustrates that OTA is the best, as slew rate is more which proves speed is more; CMRR is more which proves that common mode voltage can be easily removed.

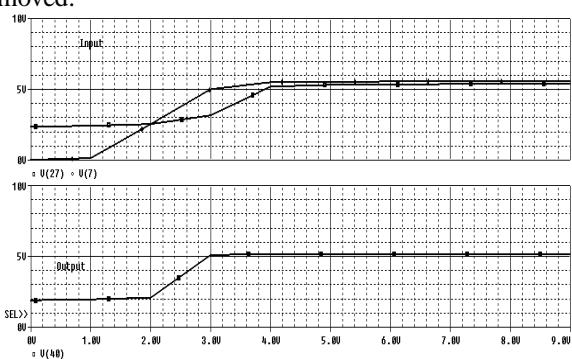


Fig 6 Output corresponding to MAX function using 2 stage CMOS op-amp

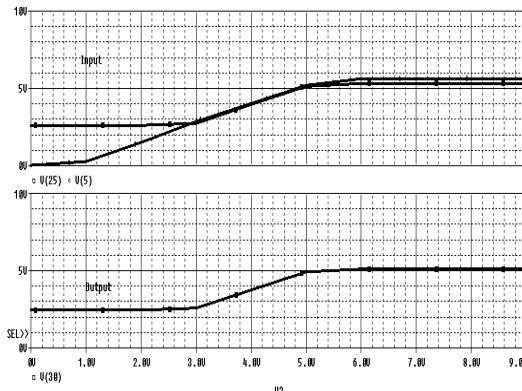


Fig 7 MAX function using OTA

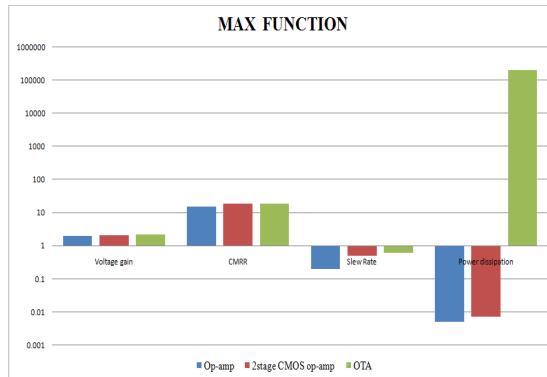


Fig 8 Comparison of MAX operator in terms of electrical parameters

2) **MIN circuit :** The MIN circuit can be realized by using diode in AND logic configuration which was given to the non-inverting terminal of an op-amp.

Let V_1 & V_2 be the two voltage inputs to the diodes D_1 & D_2 resp. The voltage at node A is the MIN of two input voltages V_1 & V_2 (AND logic). If $V_1 < V_2$, then V_1 will appear at output i.e. node A otherwise V_2 will appear. The actual voltage at node A is $V_1 + V_\gamma$ where V_γ is the biased voltage of the diode. V_A is the input to the non-inverting terminal of the op-amp. The input at non-inverting terminal should be equal to output so we have assumed the gain as 1 i.e. the output of op-amp at node B is $V_1 + V_\gamma$. To get output as V_1 , diode D_3 is attached at the output with resistance R_L in series with that. The final output is taken across load R_L so that

$$V_O = V_O - V_{D3} = V_A - V_\gamma = V_1 + V_\gamma - V_\gamma = V_1.$$

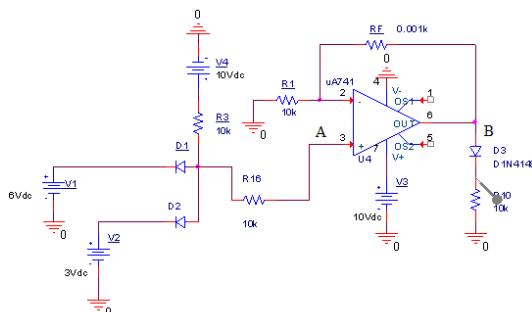


Fig 9 Circuit diagram of MIN using op-amp

Let us assume $V_2 = 3$ volts and $V_1 = 6$ volts,

then output

$$V_O = \text{MIN}(6, 3) = 3 \text{ volts shown in Fig 10.}$$

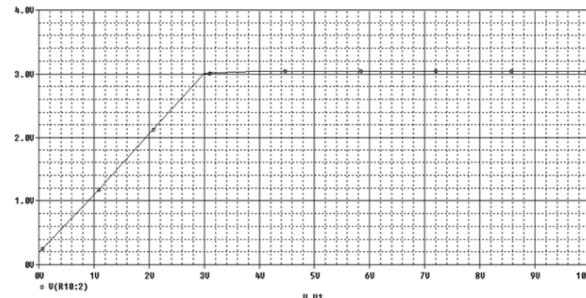


Fig 10 : Output of MIN circuit using op-amp

Similarly, we have designed MIN function using 2 stage op-amp (result shown in fig 11) and OTA (result shown in fig 12) and also calculated its electrical parameters shown in fig 13. Fig 13 illustrates that OTA is the best as slew rate is more which proves speed is more; CMRR is more which proves that common mode voltage can be easily removed.

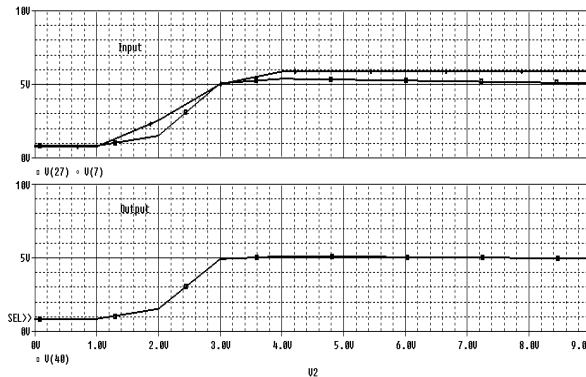


Fig 11 MIN function using 2 stage op-amp

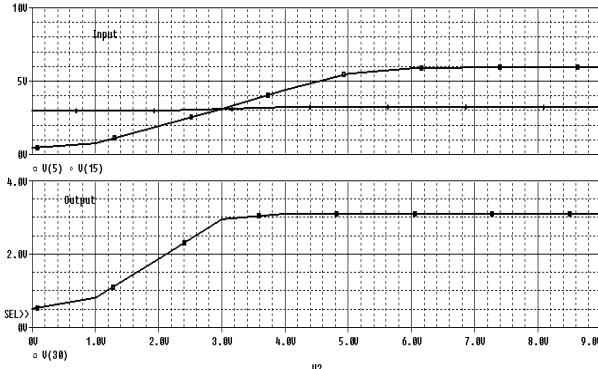


Fig 12 MIN function using OTA

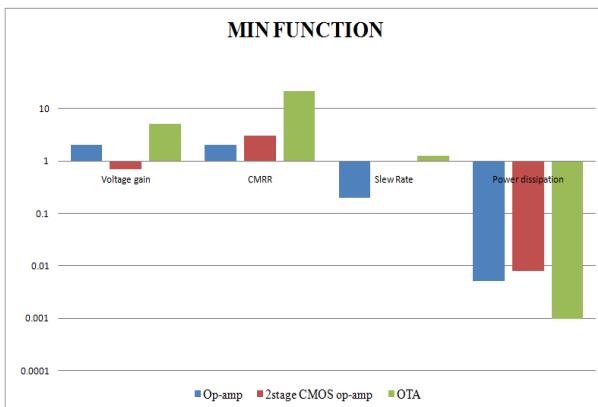


Fig 13 Comparison table of MIN operator in terms of electrical parameters

III. CONCLUSION AND FUTURE SCOPE

In this paper we have presented the electronic circuit for MIN/MAX operator using op-amp, OTA and two stage CMOS op-amp. We have also implemented the MIN/MAX functions and compare the results with two stage CMOS op-amp and op-amp using BJT. Results with OTA come out to be the best in all aspect.

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